

1/11

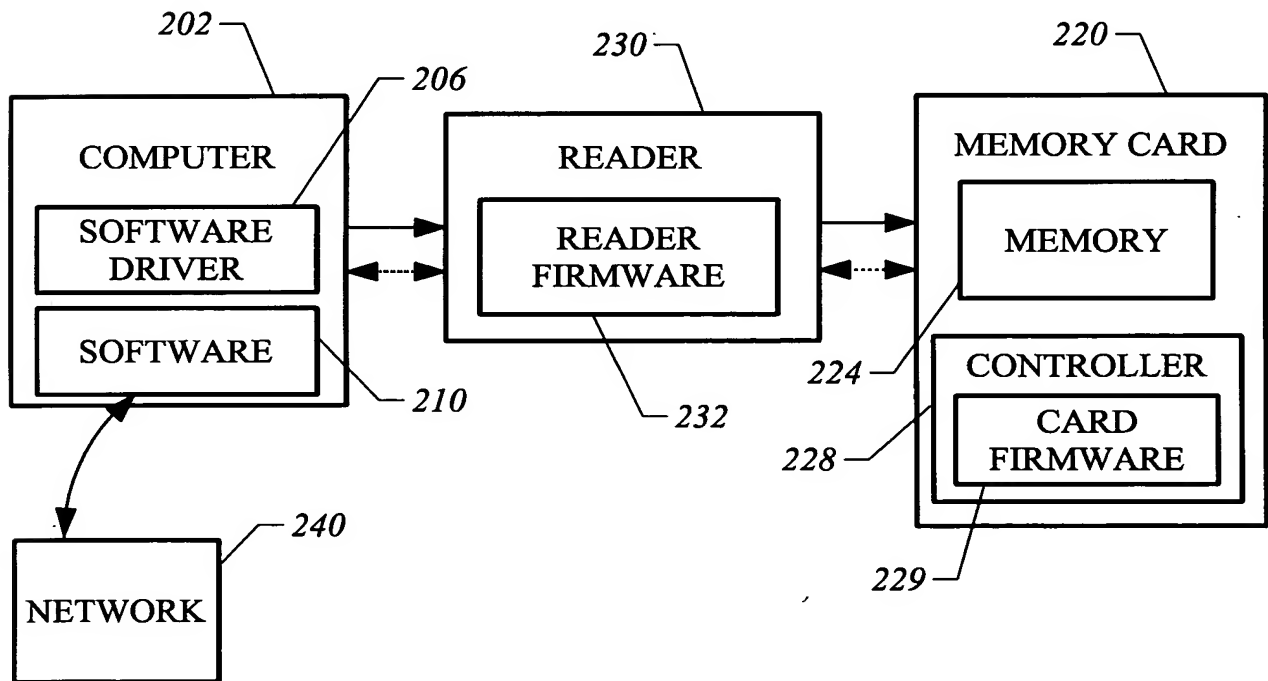
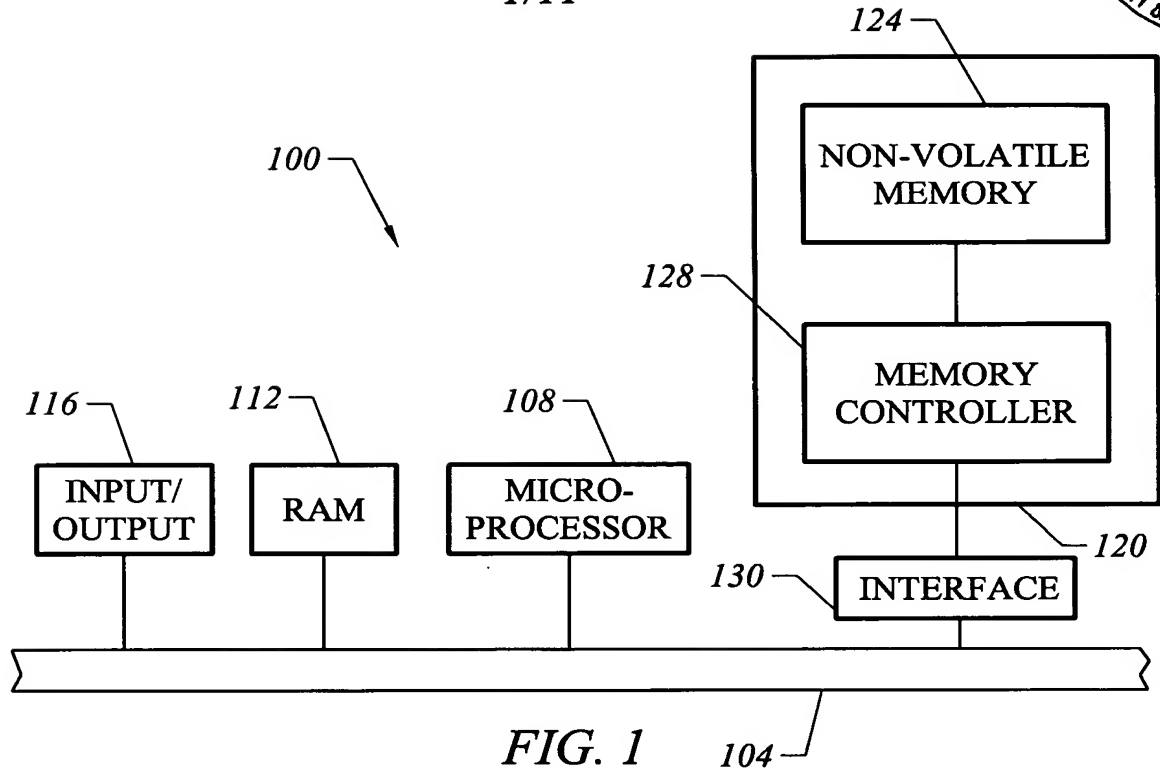


FIG. 2

2/11

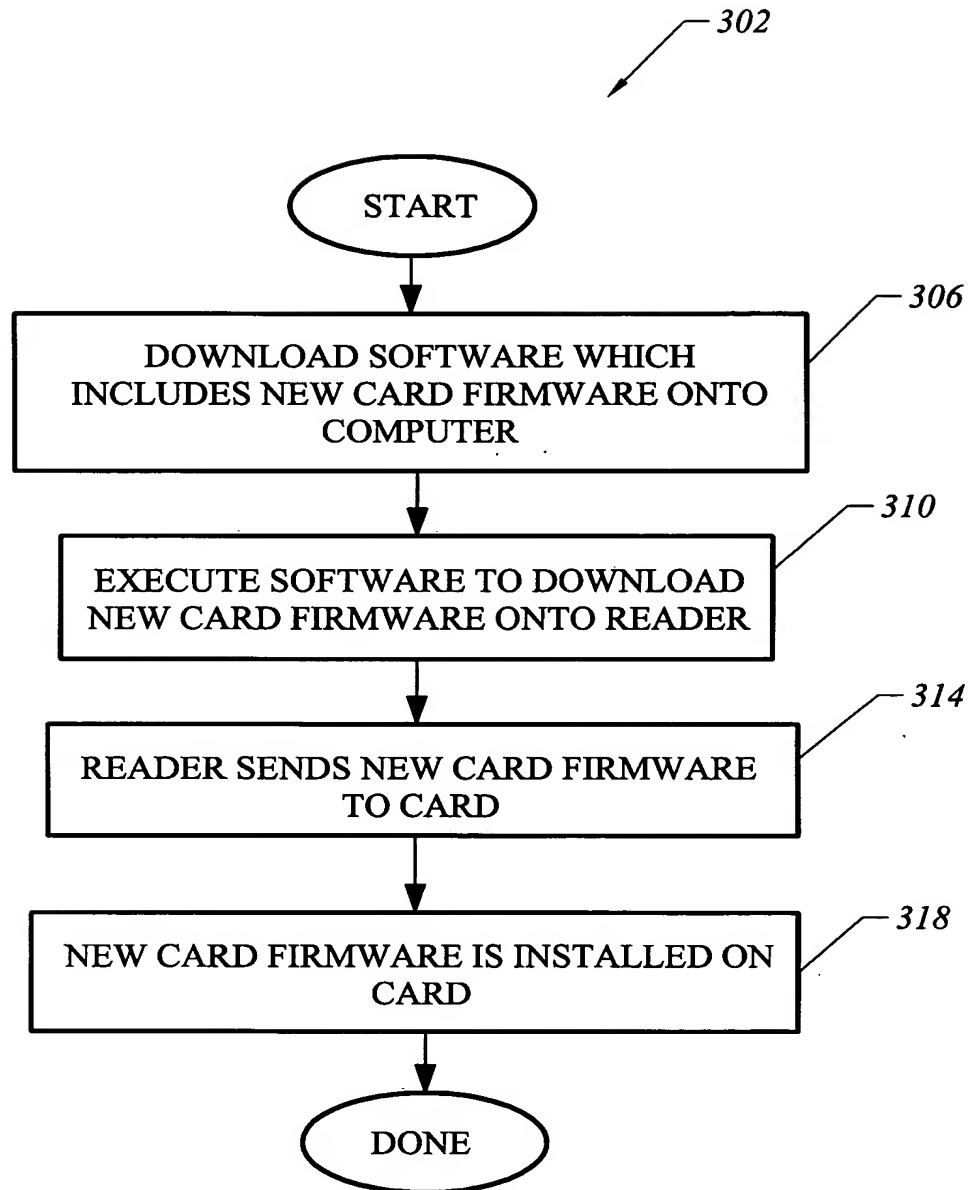


FIG. 3

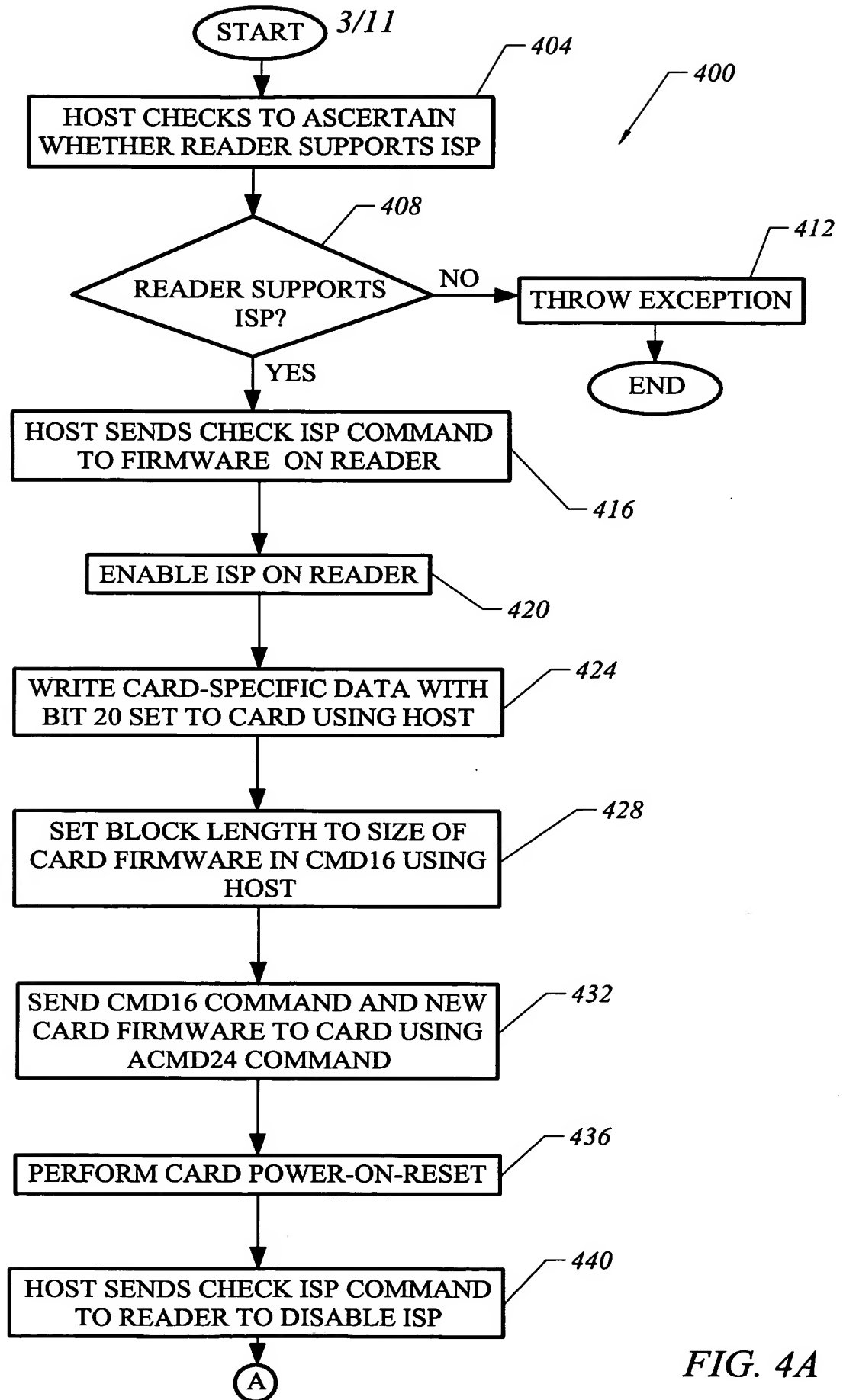


FIG. 4A

4/11

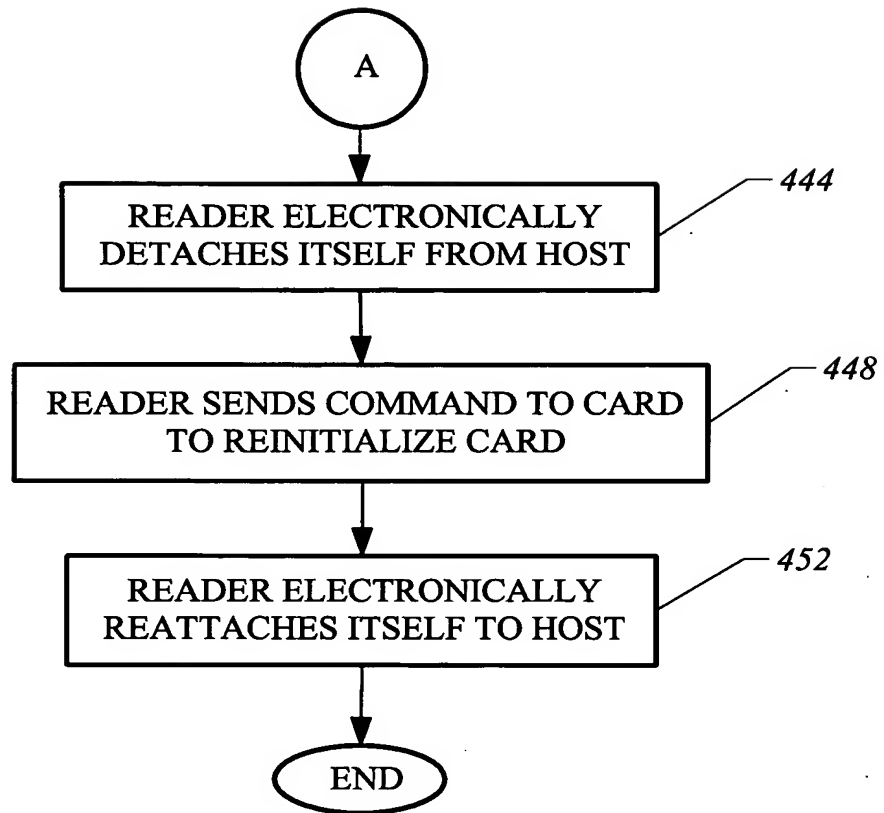


FIG. 4B

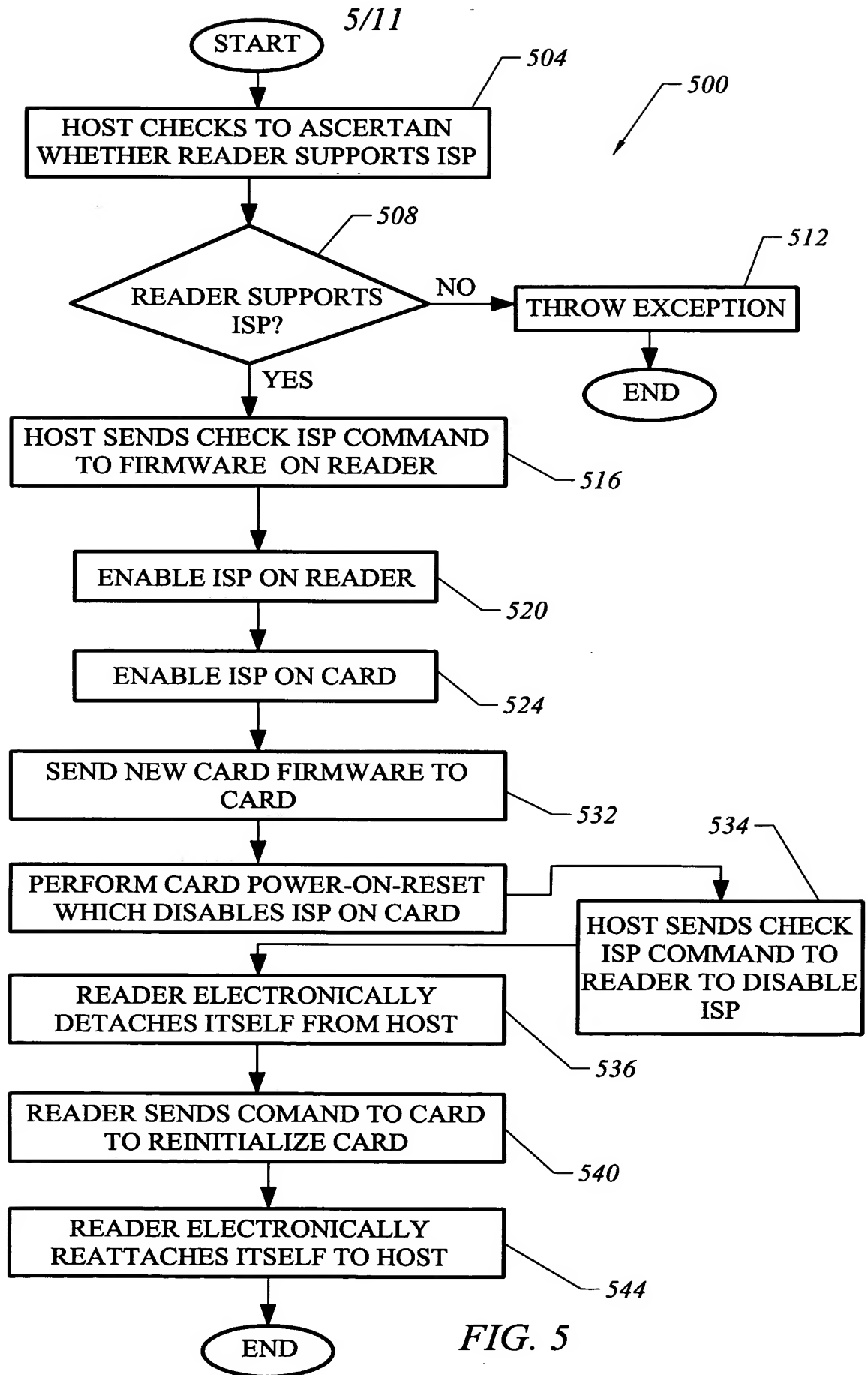


FIG. 5

6/11

		610h	610g	610f	610e	610d	610c	610b	610a
	Bit	7	6	5	4	3	2	1	0
	Byte								
606a	0	Operation Code (D0h)							
606b	1	Reserved							
606c	2	Reserved							
606d	3	Reserved							
606e	4	Reserved							
606f	5	Reserved							
606g	6	Reserved							
606h	7	Reserved							
606i	8	Reserved							
606j	9	Reserved							
606k	10	Lock	Reserved					ISP ENB	PASS ENB
606l	11								

FIG. 6A

602

		630h	630g	630f	630e	630d	630c	630b	630a
	Bit	7	6	5	4	3	2	1	0
	Byte								
626a	0	Reserved		Media Card Type				ISP ENB	PASS ENB
	1	RCA Bits 31-24							
	2	RCA Bits 23-16							

622

626b

626c

FIG. 6B

FIG. 6B

		660h	660g	660f	660e	660d	660c	660b	660a
	Bit	7	6	5	4	3	2	1	0
656a	0	Operation Code (D1h)							
656b	1	Reserved							
656c	2	Command Index							
656d	3	Command Argument 1							
656e	4	Command Argument 2							
656f	5	Command Argument 3							
656g	6	Command Argument 4							
656h	7	Data Transfer Length (MLSB)							
656i	8	Data Transfer Length (LMSB)							
656j	9	Data Transfer Length (LLSB)							
656k	10	Response Type				DIR	APP	RESP	Reserved
656l	11	Control							

FIG. 6C

652

8/11

		710h	710g	710f	710e	710d	710c	710b	710a
	Bit	7	6	5	4	3	2	1	0
706a	0	Operation Code (D2h)							
706b	1	Reserved							
706c	2	'S'							
706d	3	'D'							
706e	4	Reserved (Error Register)							
706f	5	Sector Count Register/Feature Register							
706g	6	Sector Register							
706h	7	Cylinder Low Register							
706i	8	Cylinder High Register							
706j	9	Device/Head Register							
706k	10	Command/Status Register							
706l	11	Reserved					DATA	DIR	REGS

FIG. 7A

702

752

	Bit	7	6	5	4	3	2	1	0
716c	0	'S'							
716d	1	'D'							
716e	2	Reserved (Error Register)							
716f	3	Sector Count Register/Feature Register							
716g	4	Sector Register							
716h	5	Cylinder Low Register							
716i	6	Cylinder High Register							
716j	7	Device/Head Register							
716k	8	Command/Status Register							

FIG. 7B

9/11

		808h	808g	808f	808e	808d	808c	808b	808a
	Register	7	6	5	4	3	2	1	0
812a	Feature	na							ENB
812b	Sector Count	na							
812c	Sector Count	na							
812d	Cylinder Low	na							
812e	Cylinder High	na							
812f	Device/Head	obs	na	Obs	DEV	na			
812g	Command	D5h							

FIG. 8A

804

		818h	818g	818f	818e	818d	818c	818b	818a
	Register	7	6	5	4	3	2	1	0
812a	Error	0h							
812b	Sector Count	55h							
812c	Sector Count	AAh							
812d	Cylinder Low	0h							
812e	Cylinder High	0h							
812f	Device/Head	obs	na	Obs	DEV	WP	Media Type		
812g	Status	BSY	DRDY	DF	1	DRQ	0	0	ERR

FIG. 8B

814

10/11

		828h	828g	828f	828e	828d	828c	828b	828a
	Register	7	6	5	4	3	2	1	0
832a	Error	0	0	0	0	0	0	ABRT	0
832b	Sector Count	na							
832c	Sector Count	na							
832d	Cylinder Low	na							
832e	Cylinder High	na							
832f	Device/Head	obs	na	Obs	DEV	na			
832g	Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 8C

824

		848h	848g	848f	848e	848d	848c	848b	848a	
	Register	7	6	5	4	3	2	1	0	
852a	Feature	0h								DIR
852b	Sector Count	Number of sectors								
852c	Sector Count	Starting LBA of Firmware (LSB)								
852d	Cylinder Low	Starting LBA of Firmware (Middle Byte)								
852e	Cylinder High	Starting LBA of firmware (MSB)								
852f	Device/Head	obs	na	Obs	DEV	0h				
852g	Command	D6h								

FIG. 8D

844

11/11

		868h	868g	868f	868e	868d	868c	868b	868a
	Register	7	6	5	4	3	2	1	0
872a	Error	na							
872b	Sector Count	na							
872c	Sector Count	na							
872d	Cylinder Low	na							
872e	Cylinder High	na							
872f	Device/Head	obs	na	Obs	DEV	na			
872g	Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 8E

		888h	888g	888f	888e	888d	888c	888b	888a
	Register	7	6	5	4	3	2	1	0
892a	Error	na	UNC	na	na	na	ABRT	na	na
892b	Sector Count	na							
892c	Sector Count	Error LBA (LSB)							
892d	Cylinder Low	Error LBA (Middle Byte)							
892e	Cylinder High	Error LBA (MSB)							
892f	Device/Head	obs	na	Obs	DEV	0h			
892g	Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 8F